**Execution steps:**

Execute the files circuit2.v and circuit2\_tb.v as mentioned below

Step1) iverilog -o test2 circuit2.v and circuit2\_tb.v

If the compilation went OK, you won't see any output.

What this does is create a file called   test2 that we can feed to the

simulator.

Step2) vvp test2

You can observe output on the console

Step3) gtkwave circuit2.vcd

Output waveform will be observed